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Team 8 Final Presentation CE 618 Lecture 02b: AASHTO Specifications \u0026 Limit States (2016.08.31) The Shay Murtagh Bridge Beam Technical Manual. An Engineers Tool.

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PCI has developed Preliminary Design Charts in accordance with the AASHTO. 2010. AASHTO LRFD Bridge Design Specifications, Fifth Edition with 2011 Interim Revisions. The below chart is a sample of those products. The charts can be accessed in Preliminary LRFD Design Charts which you can download below.

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A PCI bridge chip is a device that connects a PCI bus to either another PCI bus or a bus of a different standard. Peripheral component interface (PCI) is a local computer processor bus that connects peripherals to the system as if they were directly memory mapped on the processors system memory address space. PCI bridge chips are called planar devices according to the PCI specification because ...

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